

WEST Search History

DATE: Saturday, March 26, 2005

Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L11	L9 and (potential or voltage) adj2 difference	42
<input type="checkbox"/>	L10	L9 and induc\$ adj3 leakage adj3 current	1
<input type="checkbox"/>	L9	L8 and access adj2 (transistor or device)	119
<input type="checkbox"/>	L8	l4 and leakage current adj9 refresh\$	370
<input type="checkbox"/>	L7	l4 and leakage adj9 refresh\$	528
<input type="checkbox"/>	L6	L5 and (anode or source)	16
<input type="checkbox"/>	L5	L4 and access adj8 (inactive or non-activated) adj2 state	16
<input type="checkbox"/>	L4	L3 and transistor	2337
<input type="checkbox"/>	L3	L2 and leakage current	2472
<input type="checkbox"/>	L2	L1 and refresh\$	11229
<input type="checkbox"/>	L1	memory cell	66617

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER